

For : ELECTRONIC COMPONENT MOUNTING METHOD, AND CIRCUIT  
SUBSTRATE AND CIRCUIT SUBSTRATE UNIT USED IN THE METHOD

Commissioner for Patents  
U.S. Patent and Trademark Office  
Customer Service Window, Mail Stop Amendment  
Randolph Building  
401 Dulany Street  
Alexandria, VA 22314

In accordance with the duty of disclosure under 37 C.F.R. § 1.56 and §§1.97-1.98, and supplemental to the Information Disclosure Statements that were previously filed on February 23, 2009, January 30, 2009 and November 14, 2006, Applicants hereby bring to the attention of the Examiner a Japanese Office Action dated October 7, 2008 with respect to patent family member Japanese Patent Application No. 2004-054674, in which the following documents were cited:

- (1) Japanese Patent Publication No. 2004-14964 A, which is accompanied by an English language Abstract thereof; and
- (2) Japanese Patent Publication No. 2001-239395.


Applicants note that document (2) was previously cited in the Information Disclosure Statement of November 14, 2006.

A copy of above-noted Japanese document (1), including the noted English language Abstract, is attached hereto, and is listed on the attached PTO-1449 Form. A copy of the Japanese Office Action is also attached hereto. The Examiner is requested to initial the appropriate spaces on the attached Form and to return a copy of the completed Form to Applicants with the next official communication in the present application.

Applicants note that this Supplemental Information Disclosure Statement is being submitted before the mailing of a first Office Action after the filing of a Request for Continued Examination. Thus, no fee is believed necessary to ensure consideration of the submitted material.

Should the Examiner have any questions, the Examiner is invited to contact the undersigned at the below-listed telephone number.

Respectfully submitted,  
Masato MORI et al.

  
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July 30, 2009  
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